

## Sixth Semester B.E. Degree Examination, Dec.2015/Jan. 2016 Digital Communication

Time: 3 hrs .

Max. Marks: 100

## Note: Answer FIVE full questions, selecting at least TWO questions from each part.

## PART - A

1 a. With neat sketches explain flat top sampling.
(07 Marks)
b. What is Aperture effect? Explain how it can be compensated.
(05 Marks)
c. A signal $g(t)=10 \cos (20 \pi t) \cos (200 \pi t)$ is sampled at the rate of 250 samples $/ \mathrm{sec}$.
i) Sketch spectrum of sampled signal.
ii) Specify the cutoff of ideal reconstruction filter so as to recover $g(t)$ from $g_{\delta}(t)$.
(08 Marks)
2 a. Explain the block diagram of regenerative repeater.
(05 Marks)
b. A PCM system uses a uniform quantizer followed by a $\vee$ bit encoder. Show that rms signal to quantization noise ratio is approximately given by $(1.8+6 \mathrm{v}) \mathrm{db}$.
(06 Marks)
c. With neat sketch explain companding in PCM. Also explain $\mu$-law and A-law companding. (09 Marks)

3 a. Explain the following with neat sketch:
i) Slope overload distortion.
ii) Granular noise.
(05 Marks)
b. A delta modulator is designed to operate at five times the Nyquist rate for a signal with 3 kHz bandwidth. Determine the maximum amplitude of a $2 \mathrm{kHz} \mathrm{I} / \mathrm{P}$ sinusoid for which delta modulator does not have slope overload. Quantizing step size is 250 mV .
(05 Marks)
c. For the binary bit stream 10011011 draw the waveforms for the following cases:
i) Polar NRZ
ii) Manchester RZ
iii) Gray code NRZ
(05 Marks)
d. With neat sketch explain power spectra of discrete PAM signals.
(05 Marks)
4 a. What is ISI? Derive an expression for Nyquist pulse shaping criterion for distortionless base band binary transmission.
(06 Marks)
b. What is correlative coding? Explain duobinary coding with and without precoding.
(06 Marks)
c. The binary data 011100101 are applied to the I/P of a modified duo binary system.
i) Construct modified duo binary coder $\mathrm{O} / \mathrm{P}$ without precoder.
ii) Suppose that due to error in transmission, the level produced by the third digit is reduced to zero. Construct a new receiver output.
(08 Marks)

## PART - B

5 a. With neat block diagram, explain the DPSK transmitter and receiver.
(08 Marks)
b. Obtain the expression for probability of symbol error of coherent binary FSK.
(07 Marks)
c. Binary data are transmitted over a microwave link at the rate of $10^{6}$ bps and the PSD of the noise at the receiver input is $10^{-10} \mathrm{~W} / \mathrm{Hz}$. Find the average carrier power required to maintain an average prob. of error $\mathrm{P}_{\mathrm{e}} \leq 10^{-4}$ for coherent binary FSK. What is the required channel B. W? $\left(\right.$ Take erfc $\left.(3.71)=10^{-4}\right)$
(05 Marks)

6 a. Explain the Gram Schmidt orthogonalization procedure to obtain the orthonormal basis function for linearly independent set of signals.
(12 Marks)
b. Three signals $S_{1}(t), S_{2}(t)$ and $S_{3}(t)$ are as shown in Fig. Q6 (b). Apply Gram Schmidt procedure to obtain an orthonormal basis for the signals. Express the signals $S_{1}(t), S_{2}(t)$ and $\mathrm{S}_{3}(\mathrm{t})$ in terms of orthonormal basis functions. Also give signal constellation diagram.
(08 Marks)




Fig. Q6 (b)
7 a. Show that the output SNR of a matched filter is proportional to ratio of signal energy to PSD of input noise.
b. Explain the function of correlation receiver.
c. Determine the impulse response of matched filter.

8 a. Explain properties of PN sequence (max length sequence).
(06 Marks)
b. Explain the working of direct sequence spread spectrum transmitter and receiver with BPSK.
c. The direct sequence spread spectrum communication system has following parameters:

Data sequence bit duration $\mathrm{T}_{\mathrm{b}}=4.095 \mathrm{~ms}, \mathrm{PN}$ chip duration $\mathrm{T}_{\mathrm{C}}=1 \mu \mathrm{~s}$.
$\frac{E_{b}}{N_{0}}=10$ for average probability of error less than $10^{-5}$.
Calculate processing gain and jamming margin. Also find jamming margin in db. (06 Marks)


10EC/TE62

## Sixth Semester B.E. Degree Examination, Dec.2015/Jan. 2016 Microprocessors

Time: 3 hrs .
Max. Marks: 100

## Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

## PART - A

1 a. Draw the internal architecture of 8086 processor and explain in brief the execution unit and bus interface unit.
(10 Marks)
b. Explain the PSW register of 8086 .
c. List the advantages of memory segmentation of 8086 .

2 a. Determine the physical address resulting from the following instructions:
i) MOV DL, $[\mathrm{BP}+\mathrm{SI}]$
ii) MOV DI, [BX + 100h $]$
iii) SUB BOX, AX
iv) $\mathrm{MOV}[\mathrm{BP}+\mathrm{DI}+5]$, AH
v) MOV AL, [5036h]
$B P=7000 \mathrm{~h}, \mathrm{SI}=0350 \mathrm{~h}, \quad \mathrm{SS}=8000 \mathrm{~h}, \quad \mathrm{BOX}=4000 \mathrm{~h}, \quad \mathrm{BX}=4 \mathrm{FFFh}, \quad \mathrm{DS}=2000 \mathrm{~h}$
$\mathrm{DI}=6 \mathrm{~A} 00 \mathrm{~h}$.
(10 Marks)
b. Opcode for MOV instruction is 100010 . Determine the machine language code for the following:
i) MOV AL, BL
ii) MOV AL, [1234h]
(04 Marks)
c. What are assembler directives? Explain the significance of the following:
i) DW
ii) EQU
iii) ALIGN 16
iv) OFFSET.
(06 Marks)

3 a. Write a short note on string instructions.
(10 Marks)
b. Using table translation instruction WAP to find equivalent seven segment code for the given BCD digit.
(06 Marks)
c. Differentiate between Macros and Procedures.
(04 Marks)
4 a. What is an interrupt? Discuss the interrupt classification in 8086 with example.
(07 Marks)
b. Explain the response to an interrupt in 8086 .
(07 Marks)
c. Write subroutines to
i) Set trap flag
ii) Reset trap flag.
(06 Marks)

## PART - B

5 a. Explain with a neat diagram the interfacing of a $4 \times 4$ keyboard to 8086 . Draw the flow chart also. (Program not reqd.)
(12 Marks)
b. Interface a DAC AD7523 with 8086 WAP to generate a saw tooth waveform of period 1 ms with $\mathrm{V}_{\max }=5 \mathrm{~V}$. Clock frequency of $8086=8 \mathrm{MHz}$.
(08 Marks)
6 a. With a neat diagram explain the architecture of 8087 coprocessor.
(10 Marks)
b. Represent the real number $(13.75)_{\mathrm{d}}$ in a short real or in single precision representation.
(04 Marks)
c. Write a program in 8087 ALP to find the area of a circle.
(06 Marks)

7 a. Draw the block diagram of PCI interface bus and explain.
b. Write a short note on USB.

8 a. Explain with neat diagram the programming model of Intel 80386 registers.
b. Explain the memory system of 80386 with diagram.
(10 Marks)
c. Explain Branch prediction logic and cache structure of Pentium processor.


# Sixth Semester B.E. Degree Examination, Dec.2015/Jan. 2016 <br> Microelectronics Circuits 

Time: 3 hrs .
Max. Marks:100

## Note: Answer any FIVE full questions, selecting THREE from PART-A and TWO from PART-B.

## PART - A

1 a. Explain channel length modulation effect and derive an expression for finite output resistance of a MOSFET in saturation region.
(08 Marks)
b. Analyze the circuit shown in Fig. 1(b) to determine the voltages at all nodes and the currents through all branches let the nMOSFET $\mathrm{V}_{\mathrm{t}}=1 \mathrm{~V}$ and $\mathrm{k}_{\mathrm{n}}^{\prime} \frac{\mathrm{W}}{\mathrm{L}}=1 \mathrm{~mA} / \mathrm{V}^{2}$. Assume $\lambda=0$.
(08 Marks)


Fig.Q1(b)
c. Explain briefly biasing using constant current source.
(04 Marks)
2 a. Derive analytical expressions for transfer characteristics of CS amplifier.
(08 Marks)
b. Fig. Q2(b) shows a discrete CS MOSFET amplifier utilizing the drain to gate feedback biasing arrangement. Determine the small signal voltage. Gain its input resistance and the largest allowable input signal. Let $\mathrm{V}_{\mathrm{t}}=1.5 \mathrm{~V}, \mathrm{k}_{\mathrm{n}}^{\prime} \frac{\mathrm{W}}{\mathrm{L}}=0.25 \mathrm{~mA} / \mathrm{V}^{2}$ and $\mathrm{V}_{\mathrm{A}}=50 \mathrm{~V}$.


Fig.Q2(b)
(07 Marks)
c. Briefly explain common drain amplifier.
(05 Marks)
3 a. With neat circuit diagram, explain basic BJT current mirror and derive an expression for CT ratio of BJT current mirror for finite $\beta$.
b. Derive an expression for 3 dB frequency $\mathrm{f}_{\mathrm{H}}$ for an amplifier having 2 poles and 2 zeros.
(08 Marks)
c. Explain millers theorem.
(04 Marks)
4 a. Briefly explain common source amplifier with active load.
(10 Marks)
b. With neat circuit diagram, explain the MOS cascode amplifier.

## PART - B

5 a. Explain the operation of MOS differential pair with a common mode input voltage.
(07 Marks)
b. Briefly explain the basic operation of BJT differential pair with neat circuit diagram.
(07 Marks)
c. Explain two stage CMOS OPAMP.
(06 Marks)
6 a. Write a note on gain desensitivity and bandwidth extension.
(06 Marks)
b. Draw the ideal structure and equivalent circuit of the series shunt feedback amplifier and explain.
c. Write a note on amplifier with a single pole response.
(04 Marks)
7 A Derive an expression for the closed load gain ( $\mathrm{v}_{0} / \mathrm{v}_{\mathrm{i}}$ ) of the circuit shown in Fig. Q7(a). Assume the OPAMP is ideal.
(06 Marks)


Fig.Q7(a)
b. Explain instrumentation amplifier with neat circuit diagram.
(08 Marks)
c. With neat circuit diagram, explain antilog amplifier.
(06 Marks)
8 a. A CMOS inverter fabricated in a $0.25 \mu \mathrm{~m}$ process has $\mathrm{C}_{0 \mathrm{x}}=6 \mathrm{fF} / \mu \mathrm{m}^{2}, \mu_{\mathrm{n}} \mathrm{C}_{\mathrm{ox}}=115 \mu \mathrm{~A} / \mathrm{v}^{2}$, $\mu_{\mathrm{p}} \mathrm{C}_{0 \mathrm{x}}=30 \mu \mathrm{~A}, \mathrm{v}_{\mathrm{tn}}=-\mathrm{v}_{\mathrm{tp}}=0.4 \mathrm{v}$ and $\mathrm{V}_{\mathrm{DD}}=2.5 \mathrm{v}$. The W/L ratio of $\mathrm{Q}_{\mathrm{n}}$ is $0.375 \mu \mathrm{~m} / 0.25 \mu \mathrm{~m}$, and that for $\mathrm{Q}_{\mathrm{p}}$ is $1.125 \mu \mathrm{~m} / 0.25 \mu \mathrm{~m}$. The gate - source and gate drain overlap capacitances are specified to be $0.3 \mathrm{fF} / \mu \mathrm{m}$ of gate width. Further the effective value of drain body capacitances are $\mathrm{C}_{\mathrm{dbn}}=1 \mathrm{fF}$ and $\mathrm{C}_{\mathrm{dbp}}=1 \mathrm{fF}$. The wiring capacitance $C_{W}=0.2 \mathrm{fF}$. Find $\mathrm{t}_{\mathrm{PHL}}, \mathrm{t}_{\mathrm{PLH}}$ and $\mathrm{t}_{\mathrm{p}}$.
(10 Marks)
b. Implement $\mathrm{F}=\overline{\mathrm{AB}}+\overline{\mathrm{AB}}$ using AOI .
(05 Marks)
c. Explain two single input domino CMOS gate.
(05 Marks)

USN


# Sixth Semester B.E. Degree Examination, Dec.2015/Jan. 2016 Antennas and Propagation 

Time: 3 hrs .

Max. Marks:100

## Note: Answer FIVE full questions, selecting at least TWO questions from each part.

## PART - A

1 a. Define directivity. Obtain the relationship between directivity and beam area to show that smaller the beam area, larger is the directivity.
(07 Marks)
b. Define antenna aperture. Derive the relationship between aperture and beam area. ( 06 Marks)
c. Show that maximum effective aperture of a short electric dipole is equal to $0.119 \lambda^{2}$.
(07 Marks)
2 a. Find the power radiated and the directivity for the following:
i) $U=U_{m} \sin ^{2} \theta \sin ^{3} \phi \quad 0 \leq \theta \leq \pi \quad 0 \leq \phi \leq \pi$
ii) $\mathrm{U}=\mathrm{U}_{\mathrm{m}} \cos ^{\mathrm{n}} \theta \quad 0 \leq \theta \leq \pi / 2 \quad 0 \leq \phi \leq 2 \pi$
(08 Marks)
b. Obtain the relative field pattern for two isotropic point sources of same amplitude but opposite phase, spaced $\frac{\lambda}{2}$ apart.
(08 Marks)
c. State and explain power theorem.
(04 Marks)

3 a. Derive the equation for radiation resistance of a short electric dipole. (08 Marks)
b. Explain the following : i) Folded dipole, ii) Rhombic antenna.
(08 Marks)
c. A half wave dipole radiating in free space is driven by a current of 0.5 amperes at the terminals. Calculate E and H field at a distance 1 km from the antenna at angles of $45^{\circ}$ and $90^{\circ}$.
(04 Marks)

4 a. Obtain the radiation resistance of a small loop antenna.
(07 Marks)
b. Write short notes on: i) Slot antenna, (ii) Patch antenna.
(08 Marks)
c. Find the radiation efficiency of a 1 meter diameter loop of 10 mm diameter copper wire at (i) 1 MHz , (ii) 10 MHz .
(05 Marks)

## PART - B

5 a. Determine the length $\mathrm{L}, \mathrm{H}$ plane aperture and flare angles $\theta_{\mathrm{E}}$ and $\theta_{\mathrm{H}}$ of a pyramidal horn for which E-plane aperture $\mathrm{a}_{\mathrm{E}}=10 \lambda$. The horn is fed by rectangular waveguide with $\mathrm{TE}_{10}$ mode. Let $\delta=0.2 \lambda$ in the E-plane and $0.375 \lambda$ in the H-plane. Also find beam width and directivity.
(08 Marks)
b. Write short notes on: i) Lens antenna; ii) Log periodic antenna
(08 Marks)
c. Design a Yagi-Uda six element antenna for operation at 500 MHz with a folded dipole field. What are the lengths of (i) reflector element, (ii) driven element, (iii) four director element? What is the spacing between reflector and driven element?
(04 Marks)

6 a. Derive an expression for resultant field intensity in the case of a space wave propagation.
(10 Marks)
b. Evaluate the roughness factors for the earth at 10 MHz , if $\sigma=5$, for ' $\theta$ ' equal to (i) $30^{\circ}$, (ii) $45^{\circ}$, (iii) $60^{\circ}$.
(05 Marks)
c. A transmitting antenna of 100 m height radiates 40 kW at 100 MHz uniformly in azimuth plane. Calculate maximum LOS range and strength of the received signal at 16 m high, receiving antenna at a distance of 10 km . At what distance would the signal strength reduce to $1 \mathrm{mV} / \mathrm{m}$ ?
(05 Marks)

7 a. Explain the structure of ionosphere. Derive an expression for refractive index of ionospheric layer.
(10 Marks)
b. Define the following with respect to ionospheric propagation:
i) Critical frequency
ii) Virtual height
(06 Marks)
c. Obtain the relationship between maximum usable frequency (MUF) and skip distance.
(04 Marks)
8 Write short notes on:
a. Principle of pattern multiplication
b. Scanning array
c. Embedded antennas
d. Ground wave propagation


# Sixth Semester B.E. Degree Examination, Dec.2015/Jan. 2016 Operating System 

Time: 3 hrs .

Max. Marks: 100

## Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

## PART - A

1 a. List the different tasks in an operating system. Describe the different computational structures used in an operating system.
(06 Marks)
b. With a suitable timing diagrams, explain the priority assignment rule in a multiprogramming systems.
(06 Marks)
c. Define the following :
i) System call
ii) Throughput
iii) Turn-around time
iv) Response time

With a suitable curve, explain the key features and concerns of different operating system classes.
(08 Marks)
2 a. With a neat diagram, explain the layered design of operating systems.
(08 Marks)
b. Explain the structure of microkernel based operating system.
(06 Marks)
c. Write an explanatory note on virtual machine operating system.
(06 Marks)
3 a. With a state transition diagram, explain the different states of a process and its transitions.
(06 Marks)
b. With a neat diagram, explain the threads used in Solaris.
(06 Marks)
c. Discuss the problem of race condition with a suitable example. Explain the method to overcome this problem.
(08 Marks)
4 a. Discuss the methods used to achieve the memory protection with a suitable diagram.
(08 Marks)
b. Describe the memory allocation methods for the program controlled data.
(08 Marks)
c. Differentiate between contiguous and non-contiguous memory allocation methods.
(04 Marks)

## PART - B

5 a. What is demand paging? Explain the mechanism of address translation buffers with a neat diagram.
(08 Marks)
b. Explain the FIFO page replacement policy and LRU page replacement policy. Find the number of page faults for the following page reference string using these two policies.
Reference string:5,4,3,2,1,4,3,5,4,3,2,1,5
Assume page frames $=3$.
(12 Marks)
6 a. With a neat diagram, explain the facilities provided by the file system and IOCS layers.
b. Describe the organization of sequential access and direct access files.
c. Write an explanatory note on FCB.

7 a. Explain the long term, short term and medium term schedulers. Explain how these schedulers work in case of time sharing system.
(10 Marks)
b. Explain the operation of preemptive scheduling policies and its performance for the data given below :

| Process | $\mathrm{P}_{1}$ | $\mathrm{P}_{2}$ | $\mathrm{P}_{3}$ | $\mathrm{P}_{4}$ | $\mathrm{P}_{5}$ |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Arrival time | 0 | 2 | 3 | 5 | 9 |
| Service time | 3 | 3 | 2 | 5 | 3 |

(10 Marks)
8 a. Explain the Kernel actions to implement message passing using symmetric naming and blocking sends.
(06 Marks)
b. Write an explanatory note on mailboxes.
c. Explain how inter-process communication can be done in UNIX.


# Sixth Semester B.E. Degree Examination, Dec.2015/Jan. 2016 Satellite Communication 

Time: 3 hrs .
Max. Marks: 100

## Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

PART - A

1 a. List out any four advantages and four applications provided by satellites.
b. List out the frequency band disignations in common use for satellite services.
(08 Marks)
c. The apogee and perigee of an elliptical satellite orbits are 3000 km and 200 kms . Determine the eccentineity, semi-major axis and semi-minor axis.
(06 Marks)
2 a. State Kepler's three laws of planetary motion, with suitable diagram and its relevant equations.
(08 Marks)
b. With necessary diagram, define : i) Apogee
ii) Prograde orbit
iii) Inclination
iv) Retrograde orbit.
(08 Marks)
c. What is meant by sidereal time? Explain.
(04 Marks)
3 a. Explain what is meant by rain rate and how this is related to specific attenuation. (05 Marks)
b. Derive the expression for $\left[\mathrm{C} / \mathrm{N}_{0}\right]$ for the uplink.
(10 Marks)
c. An LNA is connected to a receiver to which has a noise figure of 12 dB , the cable loss is 5 dB , the LNA gain is 50 dB and its noise temperature is 150 k . The antenna noise temperature is 35 K . Calculate the overall noise temperature referred to the input. ( 05 Marks)

4 a. What is meant by the term attitude control? Explain two forms of attitude control. (12 Marks)
b. With neat block diagram, describe the TT and C facilities of a satellite communication system.
(08 Marks)

## PART - B

5 a. With the suitable block diagram, explain the function of DBS - TV receiving system intended for home application.
(10 Marks)
b. Explain and compare master antenna TV and community antenna TV.
(10 Marks)
6 a. Describe briefly the modes of interference that can occur in a satellite communication system. Distinguish carefully between satellite and terrestrial modes of interference.
(10 Marks)
b. Explain what the abbreviation "SPADE" system stands for. Explain in detail the operation of spade system.
(10 Marks)
7 a. What is meant by TDMA? Explain the need for a reference burst in a TDMA system.
(10 Marks)
b. What are different multiple access methods used in satellite communication? Briefly explain.
(06 Marks)
c. Explain bit rate for digital television.
(04 Marks)
8 Write short notes on:
a. Radar sat
b. Antenna look angle
c. GPS and its uses
d. VSat (VSAT).

# Sixth Semester B.E. Degree Examination, Dec.2015/Jan. 2016 <br> <br> Programming in C++ 

 <br> <br> Programming in C++}

Time: 3 hrs.
Max. Marks: 100

## Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

## PART - A

1 a. What are comments? Explain different types of comments in $\mathrm{C}++$ with an example.
b. Distinguish between static memory allocation and dynamic memory allocation.
c. List the salient features of object oriented programming.
d. Explain the following : \#elif, \# if def.

2 a. What are variables? Describe with an example.
(06 Marks)
b. Explain reference types and distinguish between reference types and pointer types.
(08 Marks)
c. Write a $\mathrm{C}++$ program to read integer numbers from keyboard until user enters zero and sum the even numbers only and display the result.
(06 Marks)
3 a. Write the $\mathrm{O} / \mathrm{P}$ of followings :
i) \# define square $(x) x * x$
void main( )
\{
int $i=64 /$ square (4) :
colt $\ll$ i;
\}
ii) void main( )
\{
int mum, $\mathrm{a}=15$;
mum $=---$ - a--;
cont $\ll$ mum $\ll$ a;
\}
iii) void main( )
\}
int $i$;
for $(\mathrm{i}=0 ; \mathrm{I}<5 ; \mathrm{i}++)$
\{
int $\mathrm{i}=10$;
print("\% $/ 0 \mathrm{~d}$ ", i);
i++;
\} \}
(06 Marks)
b. Write a program to find the minimum of three numbers using conditional operator.
(05 Marks)
c. Describe : break, continue and goo statements.
(09 Marks)
4 a. Describe call by value and call by reference with an example.
(08 Marks)
b. Describe inline function with an example.
(06 Marks)
c. Write a program to calculate sum of first n integers using recursion.
(06 Marks)

## PART - B

5 a. Describe exception handling in $\mathrm{C}++$ with an example.
(10 Marks)
b. Describe :
i) Rethrow
ii) Catch all handler.
(10 Marks)
6 a. Write a C++ program using class and explain public, private, object, class members.
b. Explain friend function and friend class, with example.

7 a. Discuss the principle of operator overloading with an example.
(10 Marks)
b. Write a program to overload both prefix and postfix increment operator.
(10 Marks)
8 a. Describe base class access control with an example.
(08 Marks)
b. With a program illustrate inheritance of multiple base classes.

